

PATENT**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Darrell Rinerson, et al.

Attorney Docket No.: P009.03.ABC

Patent No.: 6,906,939

Issued Date: June 14, 2005

**RECEIVED
CENTRAL FAX CENTER**Title: **RE-WRITABLE MEMORY WITH
MULTIPLE MEMORY LAYERS**

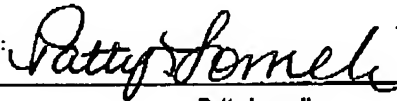
Serial No.: 10/612,191

SEP 09 2005

CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office, Fax No. (571) 273-8300 on September 9, 2005.

Signed:



Patty Lomell

**REQUEST FOR CERTIFICATE OF CORRECTION
OF APPLICANT'S MISTAKE
(35 U.S.C. §255, 37 CFR §1.323)**

Attention Certificate of Corrections Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Kindly make corrections to the above captioned issued Patent No. **6,906,939** as set forth on the attached Form **PTO/SB/44** (Certificate of Correction).

The fee pursuant to **37 C.F.R. 1.20(a)**, due in connection with the filing of this **Request for Certificate of Correction** will be paid via a Credit Card Payment Form **PTO-2038**.

09/12/2005 TL0111 00000009 10612191

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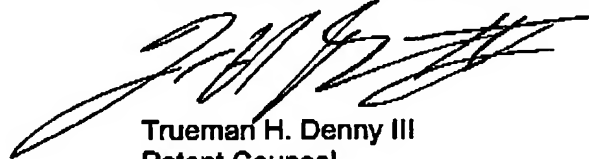
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CONCLUSION

If there are any questions regarding this Request for Certificate of Correction, the undersigned can be reached at (408) 737-7200 x124.

Respectfully submitted,

UNITY SEMICONDUCTOR CORPORATION



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PTO/SB/44 (04-06)

Approved for use through 04/30/2007, OMB 0651-0033

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(Also Form PTO-1050)**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**Page 1 of 1

PATENT NO. : 6,906,939
APPLICATION NO.: 10/612,191
ISSUE DATE : June 14, 2005
INVENTOR(S) : Darrell Rinerson, et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Kindly correct mistakes by Applicant in the above captioned issued patent as set forth in A - H below:

- A. In Col. 3, Line 36, in the "Brief Description of the Drawings", in regards to FIG. 4A, Please replace the words "four layer of memory" with the correct words "four layers of memory".
- B. In Col. 16 and Col. 17, in regards to Claim 5, Please replace ALL instances of "x-direction conductive array layers" with "x-direction conductive layers" and replace ALL instances of "y-direction conductive array layers" with "y-direction conductive layers".
- C. In Col. 17, in regards to Claim 6, Please replace "x-direction conductive array layers" with "x-direction conductive layers".
- D. In Col. 17, in regards to Claim 7, Please replace ALL instances of "x-direction conductive array layers" with "x-direction conductive layers".
- E. In Col. 17, in regards to Claims 8 and 9, Please replace ALL instances of "x-direction conductive array layers" with "x-direction conductive layers" and replace ALL instances of "y-direction conductive array layers" with "y-direction conductive layers". Please also replace ALL instances of "y-direction conductive array line" with "y-direction conductive layer".
- F. In Col. 17, in regards to Claim 12, Please replace "four memory cell array" with "four memory cell arrays".
- G. In Col. 18, in regards to Claim 21, Please replace "the re-writable is accessed" with "the re-writable memory is accessed".
- H. In Col. 18, in regards to Claim 24, Please replace "x-direction conductive array line" with "x-direction conductive layer" and replace "y-direction conductive array line" with "y-direction conductive layer".

MAILING ADDRESS OF SENDER (Please do not use customer number below):

UNITY SEMICONDUCTOR CORPORATION
250 N. WOLFE ROAD
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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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